

Serial No. 09/878,104

TRW Docket No. 12-1101

coupler 32 is used as a RF input port 34. The other input terminal is terminated to an input resistor 36. One output terminal of the Lange coupler 32 is coupled to the input of the carrier amplifier 22 while the other output terminal is coupled to the input to the peak amplifier 24. A $\lambda/4$ impedance transformer having a characteristic impedance $Z_0 = 2R_L + R_{opt}$ is provided at the output of the amplifiers 22 and 24. An output terminal of the power amplifier 20 is terminated to load impedance R_L . Both the carrier amplifier 22 and the peak amplifier 24 are configured to deliver maximum power when the load impedance R_L is R_{opt} .

Please amend paragraph [0020] on page 5 as follows:

[0020] The matching networks 26 and 28 are serially coupled to the outputs of the carrier and peak amplifiers 22 and 24, respectively. These matching networks 26 and 28 may be provided as low pass networks, for example, as illustrated in Figs. 5A-5C. As shown in Figs. 5A-5C, the matching networks 26, 28 may be implemented as a series inductance 40 or transmission line 42 and a shunt capacitance 44 or open stub 46. In operation, when the carrier amplifier 22 is on and the peak amplifier 24 is off, the matching networks 26, 28 provide a relatively high impedance (mainly due to the high impedance transmission line 42 or inductance 40) such that the peak amplifier 24 does not load down the carrier amplifier 22, operating in class A, to achieve optimum linearity and efficiency under low input power conditions.

Please amend paragraph [0022] on page 5 as follows:

[0022] Various biasing networks can be used for tuning the carrier and peak amplifiers 22 and 24. Exemplary biasing networks 48 and 50, are illustrated in Figs. 6A and 6B. Each of the biasing networks 48, 50 include a biasing resistor, R_{bbc} or R_{bbp} , coupled to an external source of DC, V_{bc} or V_{vp} . A low pass capacitor C_{cpl} or C_{plp} is coupled to the biasing resistor, R_{bbc} or R_{bbp} , the external source DC voltage, V_{bc} or V_{vp} , and ground to filter out noise. Coupling